

Notice of References Cited	Application/Control No. 09/702,462	Applicant(s)/Patent Under Reexamination DAVIS ET AL.	
	Examiner David J. Huisman	Art Unit 2183	Page 1 of 1

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	B	US-6,237,077	05-2001	Sharangpani et al.	712/24
	C	US-6,137,836	10-2000	Haataja, Tero	375/240.08
	D	US-5,958,044	09-1999	Brown et al.	712/219
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NON-PATENT DOCUMENTS

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	U	Hennessy and Patterson, "Computer Architecture - A Quantitative Approach, 2nd Edition," 1996, pp.130, 134, 163, and 168-169
	V	Texas Instruments, "TMS32010 User's Guide," 1983, pg.3-7
	W	Rogers et al., "Supporting Dynamic Data Structures on Distributed-Memory Machines," March 1995, pp.234-263
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.